

Macro Replacement

Sub-array Replacement

Row and Col Replacement

Yield (%)

Capacity (bit)

16M 64M 256M 1G

Fatal fault

Isa fault

Bit & line fault

Subarray replacement

$D = 2 \text{ cm}^2$

(1) Fatal Fault

(2) Stand-by Current Fault

(3) Bit & Line Fault

FIG. 1  
(Prior Art)

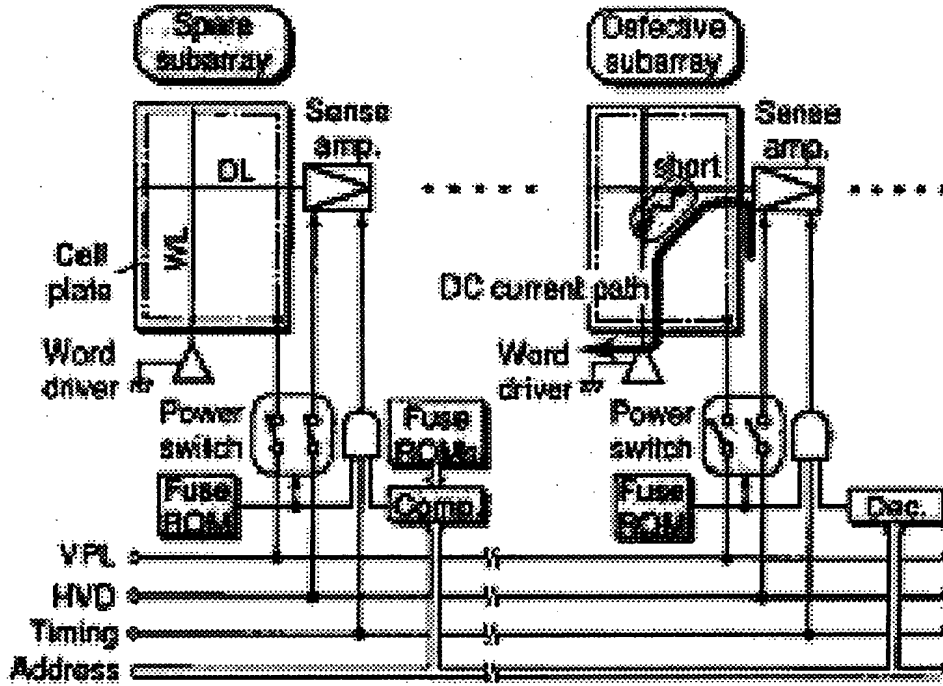


FIG. 2  
(Prior Art)

0929591.034401

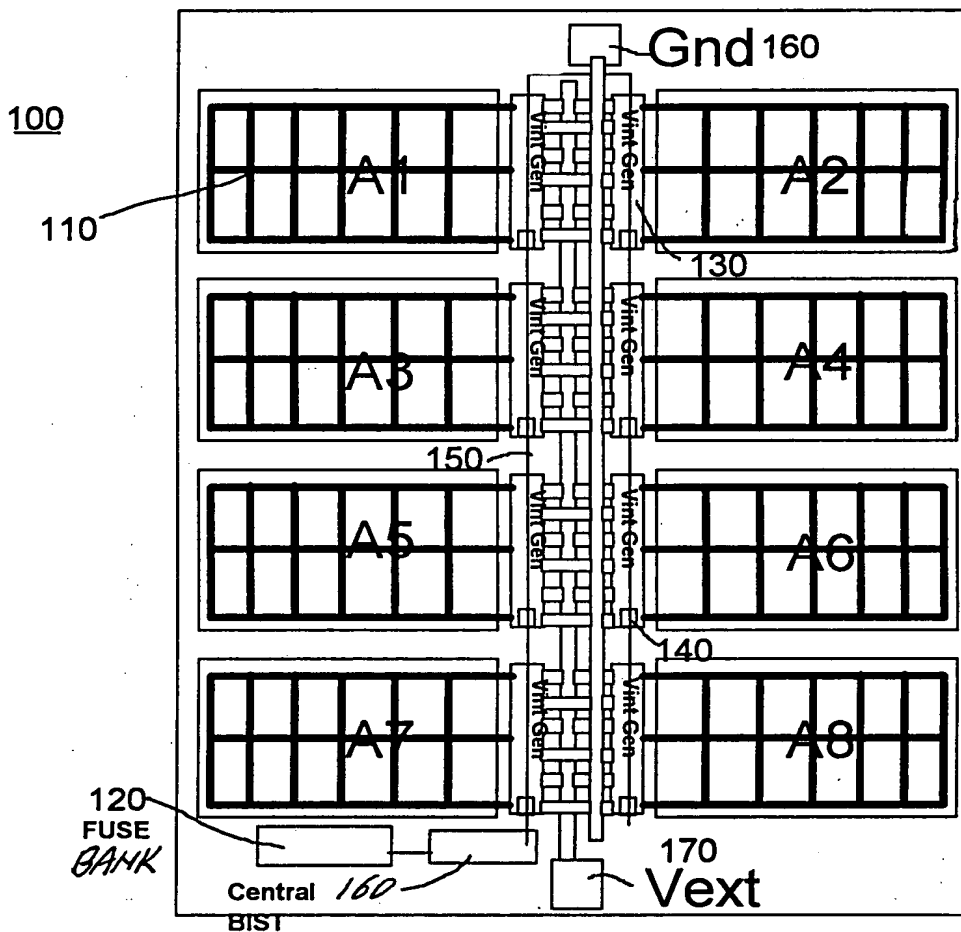


FIG. 3

0929591.081401

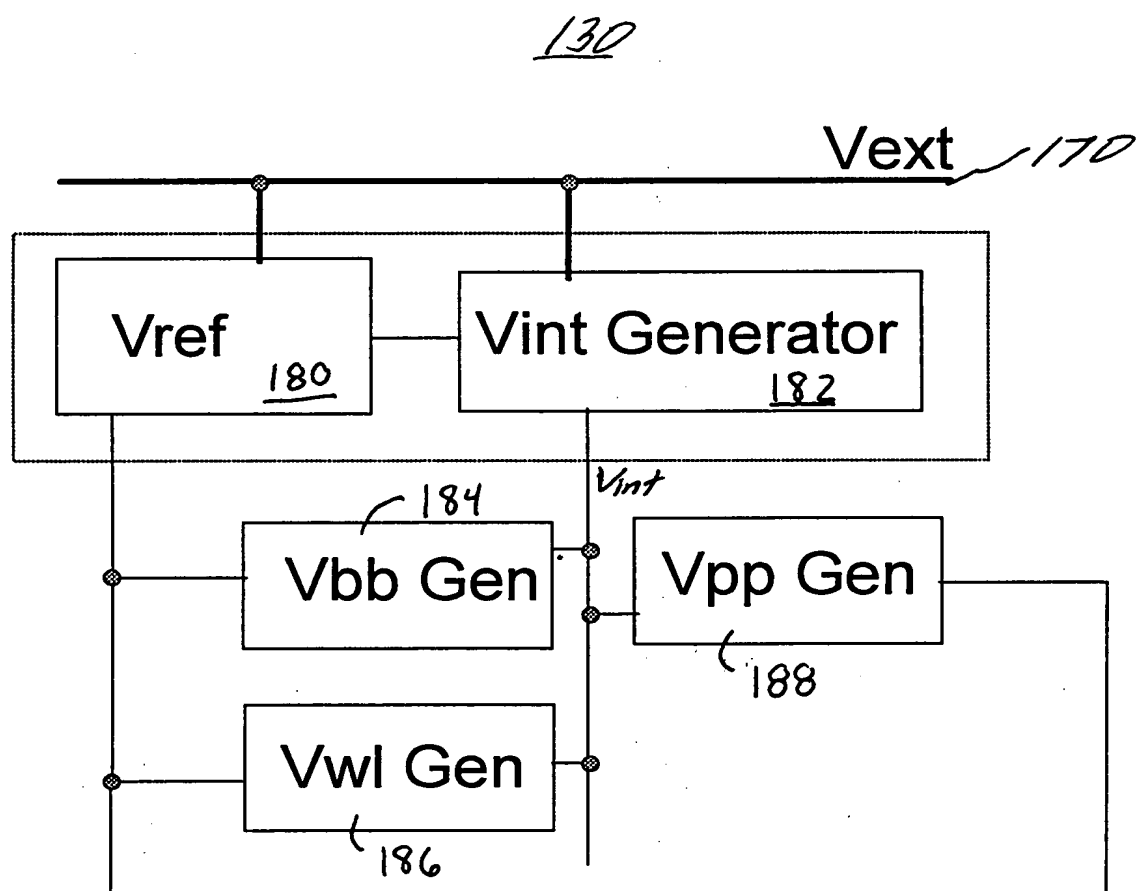


FIG. 4

0952591.081401

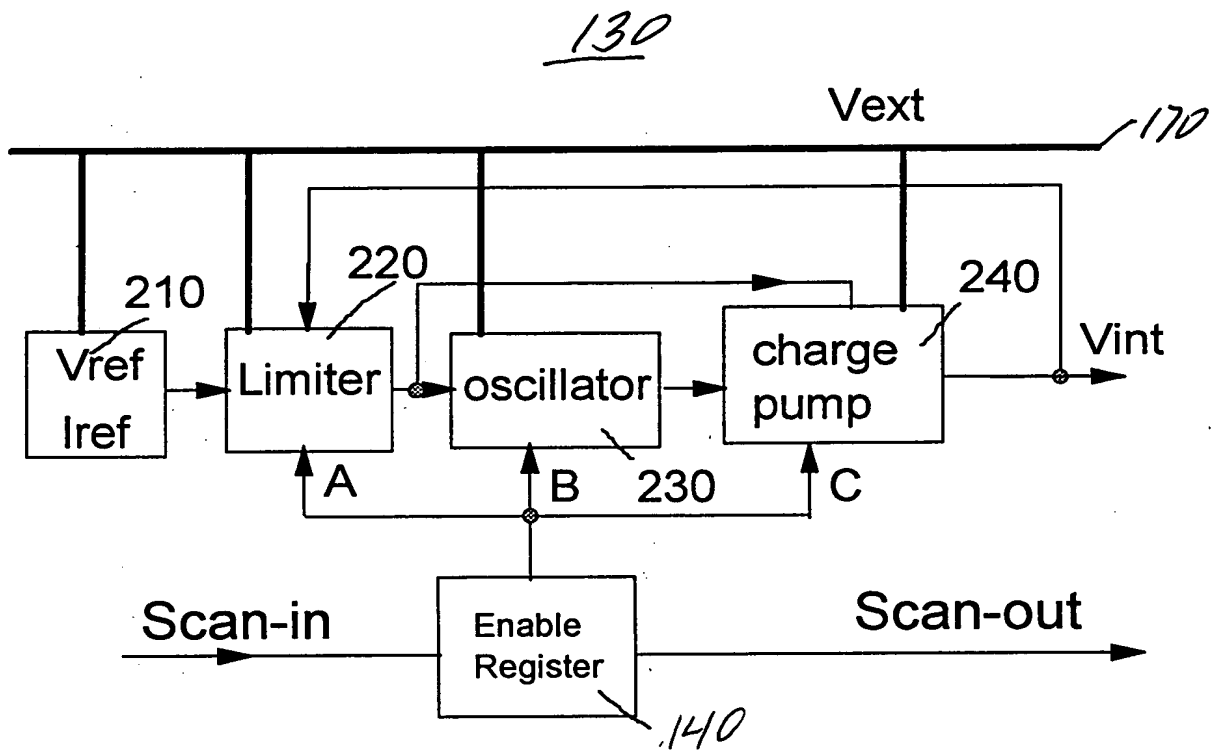


FIG. 5